

Fig. 1

Queue Manager Software Interface

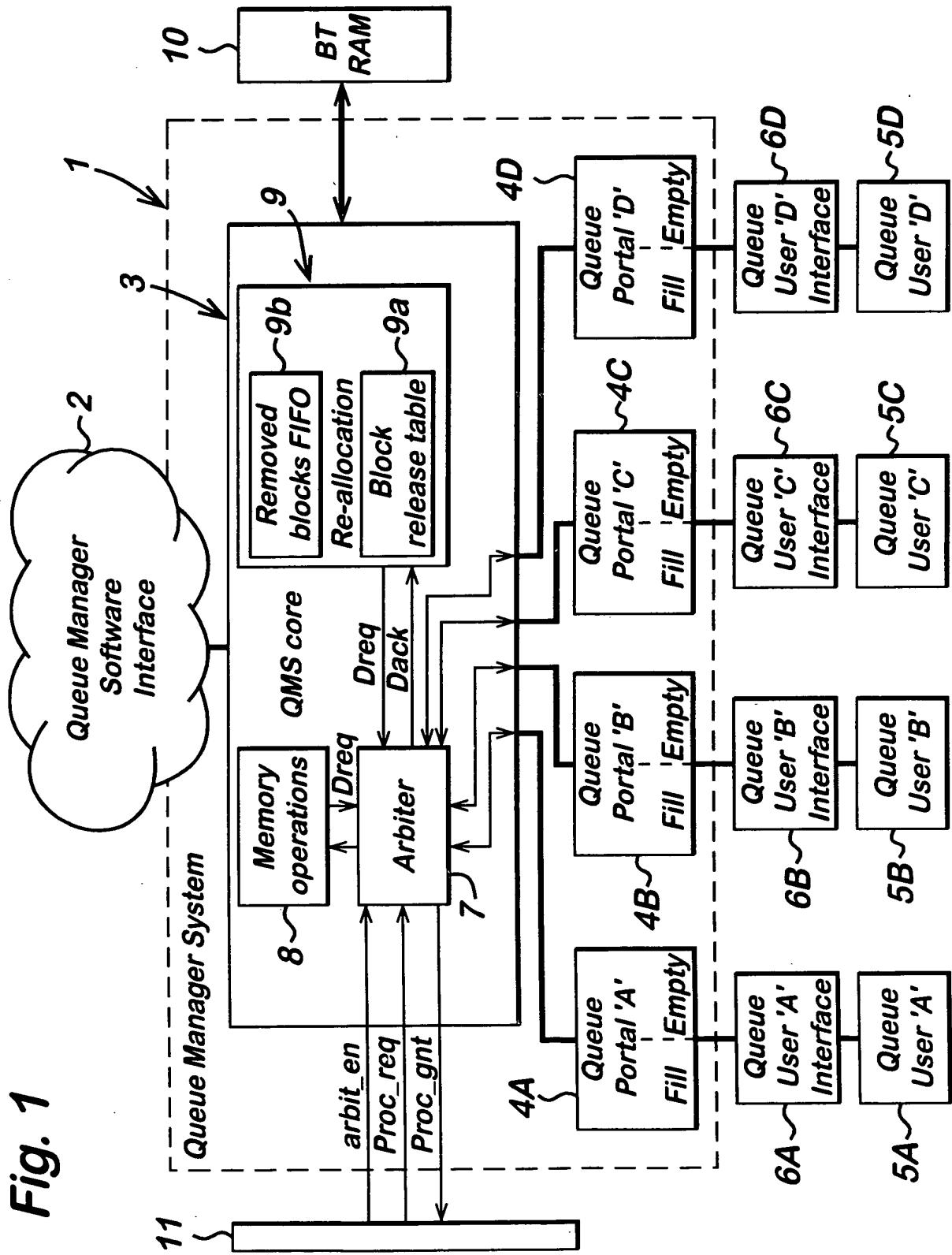


Fig. 2A

2/5 *Bus with multiple masters (and slaves);-*

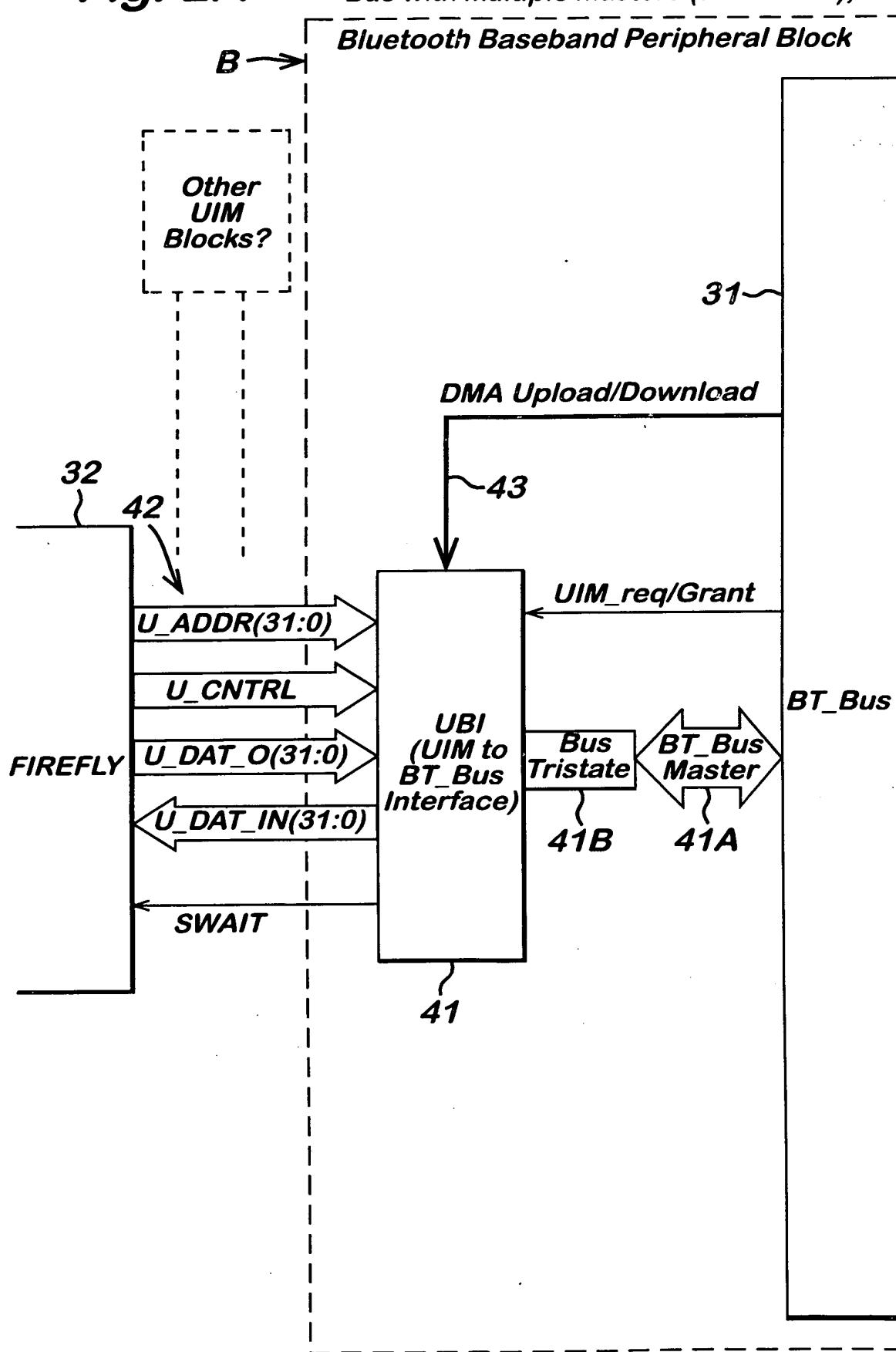


Fig. 2B

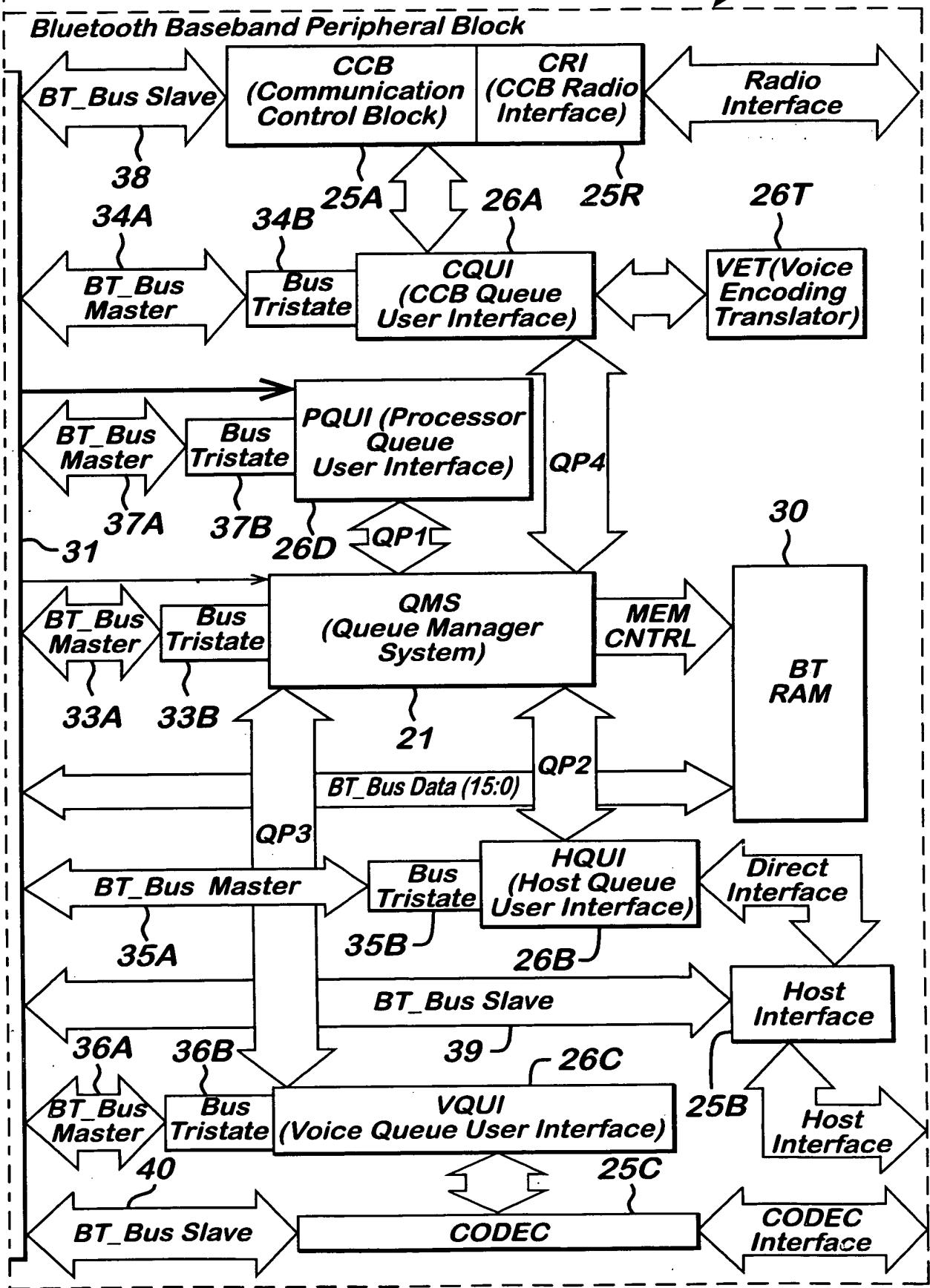
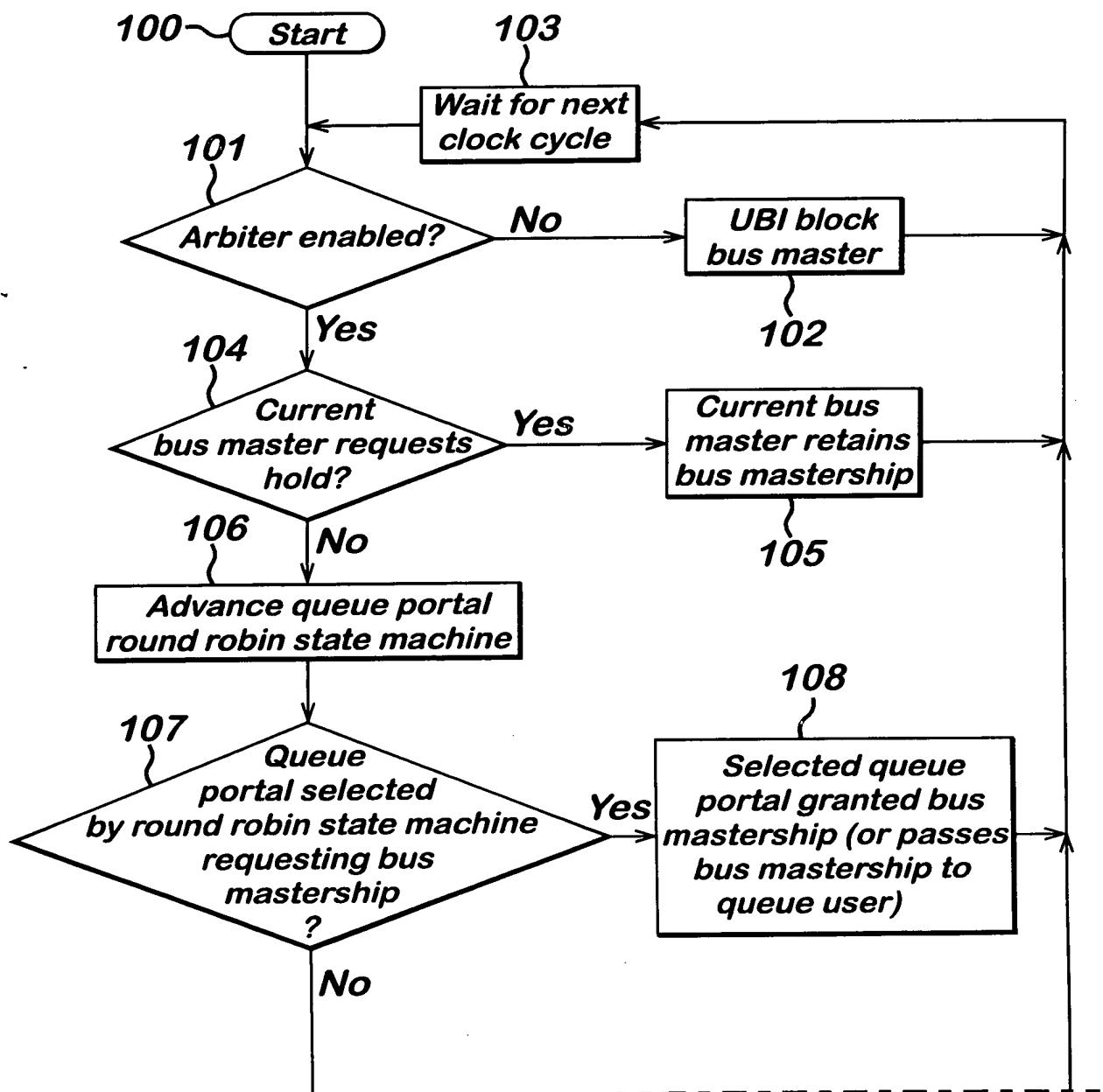


Fig. 3A Arbiter state machine



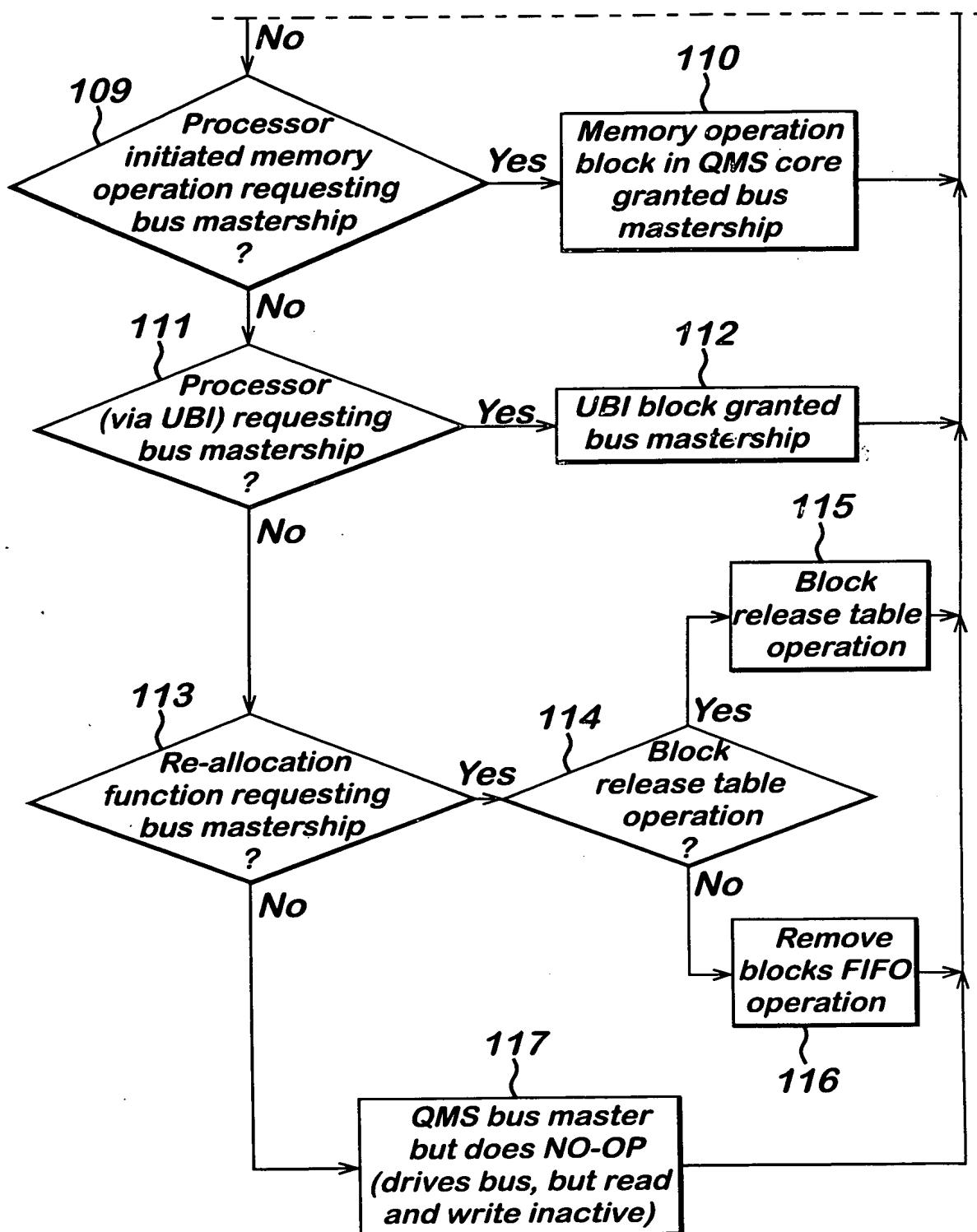


Fig. 3B